



### eBeam Initiative Luncheon PMJ – April 19, 2012

Aki Fujimura CEO – D2S, Inc. Managing Company Sponsor – eBeam Initiative

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### eBeam Writes All Chips

#### The eBeam Initiative:

- Is an educational platform for all lithography approaches including Maskless and Imprint
- Open to any company in the semiconductor design chain with an interest in eBeam technologies





#### **43 Member Companies & Advisors**





# 2010 Design for eBeam (DFeB) Roadmap:



14nm



**Courtesy : DNP** 

2011 Design for eBeam (DFeB) Roadmap: Sub-80-nm Discontinuity Has Arrived





The old assumption : Dose Margin is independent of shape The new world : Dose Margin depends on shape and size

### 2012 Design for eBeam (DFeB) Roadmap: Importance of Mask CD Uniformity



#### **Roadmap Themes**

- Mask write times
- Total cycle times
- Mask accuracy
- Wafer quality
- Design for eBeam Methodology

#### 2010

- Complex masks
- Overlapping shots circles, VSB
- Model-based mask data prep (MB-MDP)

#### 2011

- Thermal analysis of overlapping shots
- Dose control for accuracy
- Double simulation for more accurate analysis of wafer quality

#### 2012

- Mask CD Uniformity improvements
- Accurate measure of mask goodness
- Full chip MB-MDP

#### 2013

- Design for eBeam (DFeB) mask methodology
- Incorporating eBeam Initiative technology roadmap



### eBeam Technologies to Improve Mask CDU

- Dose Modulation
- Mask Process Correction (MPC)
- Model Based Mask Data Prep (MB-MDP)
  - Enables overlapping shots, dose modulation and circular (or any shape) shots
- Circular eBeam Shots
  - Requires MB-MDP and machine support



### **Today's Speakers**

- CD Uniformity Improvements using VSB Shots
  - Ryan Pearman, D<sub>2</sub>S, Inc.
- MB-MDP Impact on Mask Accuracy and Write Times
  - Yasuki Kimura, HOYA
- Proof Point on MB-MDP and Wafer Quality Simulation
  - Gek Soon Chua, GLOBALFOUNDRIES
- Q&A





### CD Uniformity Improvements Using VSB Shots

#### Ryan Pearman Director of Modeling – D2S, Inc. Bob Pack

www.ebeam.org





## **Critical Dimension Uniformity (CDU) on Mask**



From a wafer fab perspective, improving CD accuracy is important. Reducing CD variability is key What can the Mask Shop contribute to our customer's success?









Worse Dose Margin when printed features deviate from drawn

- Small features
- Line-ends
- Sharp corners
- Tight pitch

Conventional MPC cannot address problem MB-MDP overlapping shots creates margin





# Complex ILT with non-orthogonal SRAFs









Beam



**D**2S Overlapping VSB shots provide options Beam

- In the past, we have not had to think about shot optimization. There was really only one way to do things.
  - Shape defines your shot placement
    - VSB restricted to manhattan (0,90,+/-45) angles
    - Arbitrary shapes at a cost of finer manhattanization
      - Costs in both write time and CDU.
  - Minimize overall number of shots (write time)
  - Co-optimize CD-split minimization with sliver avoidance (CDU)
    - Cost to write time for this
- Introduction of overlapped VSB shots provides an extra degree of freedom
  - Can it be possible to improve on all three elements at the same time?
    - Reduce or remove shape restrictions?
    - Simultaneously write a mask faster while improving overall CDU?







- Monte Carlo simulation of effect of shot and dose variability on many long 30 degree ILT srafs
  - Vary dose (σ=5%)
  - Vary position ( $\sigma$ =1.5nm)
  - Vary shot density (50-100% of conventional shots)
    - Affects: aspect ratio of rectangles, degree of overlap





## **CDU** at constant pitch









**Optimal amount of VSB** overlap which minimizes overall CDU at a given shot density.

# **Determining the optimal shot**









#### Model-Based Mask Data Preparation Impact on Mask Accuracy & Write Times

Mask Division HOYA Corporation 19 April 2012 eBeam Initiative Lunch

# <u>Motivation</u>

- Fidelity Limit
- Too Much Writing Time for SMO mask.
   14nm node needs 2X~8X shot counts than 22nm node
   @ previous eBeam Initiative Lunch Meeting
- Limit of Rule Base Correction of SRAF. CD of Main pattern is closing to CD of SRAF. we shall apply model base correction to main.

# **Limit of Fidelity**



# More smaller $\beta_{beam}$ is required.

#### HOYA does not agree too high current ! Advanced Resist



Beam Blur  $\beta_{beam}$  > Resist Blur  $\beta_{Resist}$ 

# Advancement of Resist decrease to enhance Resolution.

 $\beta_{eff}: Effective \ Blur, \ \beta_{beam}: Beam \ Blur, \ \beta_{Resist}: Resist \ Blur$ 

## **Why MB-MDP is Good for Mask Making**

Requirements: Shot count reduction, Fidelity Enhancement, Works well with our machine

3Dimension 4Dimension

To achieve challenging requirements, we need as many dimensions of correction flexibility as possible! MB-MDP has the most dimensional control.

### **Experimental**

MB-MDP condition β<sub>eff</sub>=25nm, Eth=0.5 Single Gaussian

Test Pattern Phase 1: Qualitative analysis

SMO

3D: Dose, Figure, Overlap Phase 2: Quantitative analysis

Diagonal
 2D: Figure, Overlap

#### Error Analysis

- Per Pixel Edge Error.
  - Compare EB-simulated image by shots with target image.



- Overlay Image
  - Overlay to check visibly.





### 1/8 Shot Count Reduction Less than Conventional !

| Per Pixel Edge Error<br>(nm) |       |      |
|------------------------------|-------|------|
| Mean                         | Sigma | Max. |
| 0.01                         | 1.36  | 6.78 |

### **SMO (Qualitative analysis)**

8nm Gap on Mask exists in original SMO design



Best Separation Smaller CD

HOYA

EB-simulated image of MB-MDP Best CD Bad Separation

New type of DRC & MRC are necessary. More Model calibration is progressing to realize this pattern on Mask.

exposed with JBX-3200

## **Diagonal ( Qualitative analysis)**

- Isolated Line for each CD & angle combination
- CD 40~200nm
- Angle 0~165° step 15°
   Too much data, So we report only 135°.
- Line Length 20um
- 2D: Figure, Overlap Same shot rank in One Line.
- JBX-3200 exposed Diagonal.
- Basic Dose of Block changes.
- CD, LWR, Dose Margin
- MB-MDP Aims Dose Margin



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#### **MB-MDP** Fracturing Policy

HOYA

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| Data Set     |   |
|--------------|---|
| Conventional | Rectangle with pitch 48nm≒2 <b>≭</b> βeff   |
| MGN1         | Margin optimized width same pitch of Conventional   |
| MGN2         | Margin optimized with wider pitch (60nm)<br>for 20% shot count reduction from Conventional. |

#### **Comparison of MGN-Pattern SEM image**



•Measured CD : 200nm

SEM images are appleid scan rotation

#### LWR & Shot Count Results of 135° Diagonal



Measured CD is 200nm, Box : 2900nm, 256Samples

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#### **Dose Margin Results of 135° Diagonal**



Measured CD is 200nm

## <u>Conclusion</u>

- Model-based correction is required for Shot Count Reduction and Fidelity Enhancement.
- High Dimensional Correction is required to satisfy the increasingly difficult requirements.
- MB-MDP dramatically reduces shot count for complex patterns.
- HOYA found MGN1 has better Dose Margin! Now analyzing results more and will show them in detail at BACUS!
- More Model calibration is progressing.

# Proof Point on MB-MDP & Wafer Quality Simulation

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**GLOBAL**FOUNDRIES

April 19, 2012



- Motivation
- Results & discussion
  - Example 1: 20nm Non-ILT Metal LOGIC
    - Lithography simulation verification results
    - CD uniformity check (new during PMJ 2012)
  - Example 2: 20nm ILT Via SRAM
    - Quality & mask shot count reduction trade-off
    - Lithography simulation verification results

Conclusions







- Current solution is a step-by-step solution, OPC → Fracturing → VSB format with MPC, which contrains each other and eventually limit in maximizing Wafer PW.
  - MRC limits OPC flexibility
  - Mask Process (resolution limit, corner rounding) limits Free-Form OPC
  - Flat OPC signature requirement to mask limits Mask Process Optimization, such as mask resolution or CDU enhancement.
- It is ideal if we have a solution to optimize above interactive factors at the same time.

#### Example 1: Model-Based Mask Data Preparation (MB-MDP) for non-ILT metal



- Images show shot configuration and simulated mask contours for Main Features (left) and SRAFs (right)
- There are no abrupt mask contour differences for the main feature while maintaining close to 20% mask shot count reduction
- □ While shot count for purely rectangular SRAFs cannot be further reduced, MB-MDP can correct the CD non-linearity observed at such small dimensions and get the SRAFs back to target.
- The simulated CD loss for those 60nm SRAFs is 5nm which gets corrected during the MB-MDP step.

# **Example 1:** Description of test case preparation for non-ILT layout



# Example 1: Litho simulation verification on 20nm metal LOGIC

- MB-MDP improves shot count by 20%
- □ MB-MDP improves wafer CDU for some patterns by improving mask fidelity for small SRAFs
- PV Band, MEEF, and DOF are slightly improved with MB-MDP



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# Example 2: Description of test case preparation for ILT layout



# Example 2: Litho simulation verification on 20nm Via SRAM



## Example 2: 20-nm ideal ILT SRAM via patterns





# Example 2: Wafer quality vs. mask shot count



- Conventional Fracturing: Manhattanized ILT required 13,000 shots and a 5nm resolution to get within 10% in PV Band of "ideal"
- MB-MDP: Using overlapping shots, the worst-case PV Band improved to the level of the "ideal" OPC for <3000 shots</p>
- Litho performance similar to "ideal" shapes can be achieved by writing curvilinear shapes with MB-MDP
- MB-MDP improves the trade-off curve of litho performance vs. mask write times:
  - □ "Ideal" shapes are production worthy with this approach



- Overlapping shots created by MB-MDP enable lowered shot count (and therefore faster write times) while simultaneously maintaining or improving lithography process window on the wafer.
- <u>MB-MDP simulates the effects of shots to produce the OPC-desired contour on the mask</u> <u>plane</u>. This is effective to reduce shot count for complex masks generated by technologies like Source-Mask Optimization (SMO)/ Full Chip Mask Optimization (FCMO), or Inverse Lithography Technology (ILT).
- <u>The effectiveness of MB-MDP is verified on 20nm Non-ILT Metal LOGIC</u>. As main features & SRAF are corrected by MB-MDP, 20% shot count savings and wafer CDU improvement are achievable.
- <u>The effectiveness of MB-MDP is verified on 20nm ILT Via SRAM</u>. The result shows that MB-MDP can reduce variation in a 20nm SRAM contact level layout based on "ideal" inverse lithography patterns. The DOF, CDU, PV-band and MEEF are all dramatically improved while at the same time reducing the shot count by 33%.





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