

# Simulation-Based Mask Inspection and Review for the 10nm Node and Beyond

## Linyong (Leo) Pang D<sub>2</sub>S, Inc. April 20, 2015



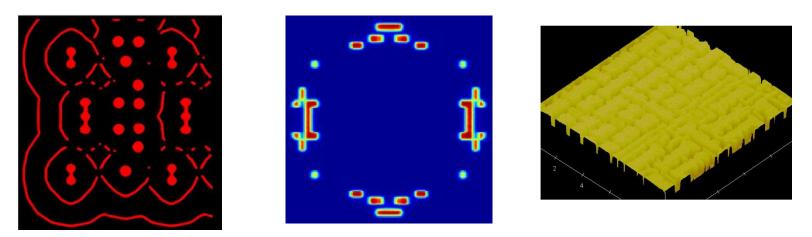
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## 193i Needed to be Extended...and Extended

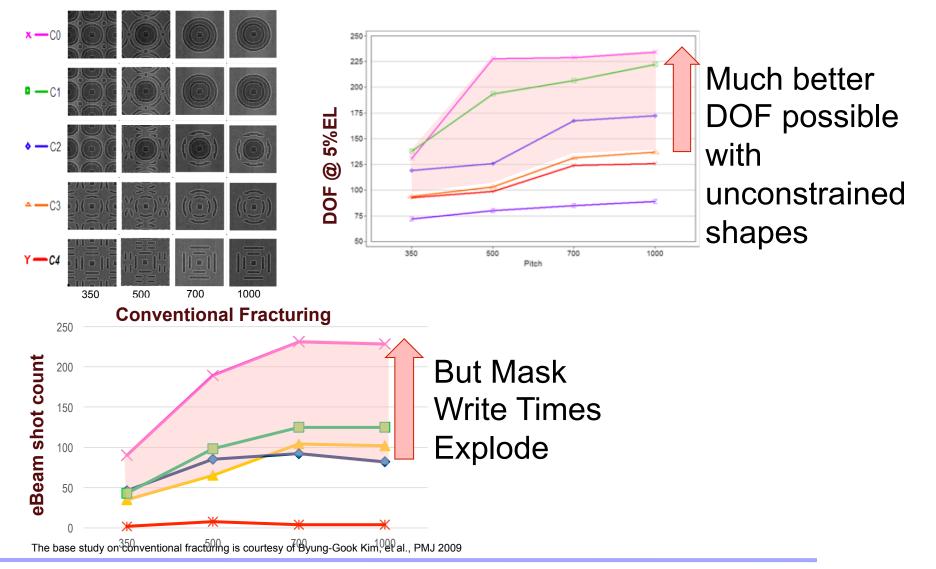
- Inverse Lithography Technology (ILT), Source-Mask Optimization (SMO), and Pixelated Masks invented
- The enabler is mask with small (assist) or/and complex features



Source: SPIE 8680-3 Source: SPIE7640-4 Source: SPIE6924-13

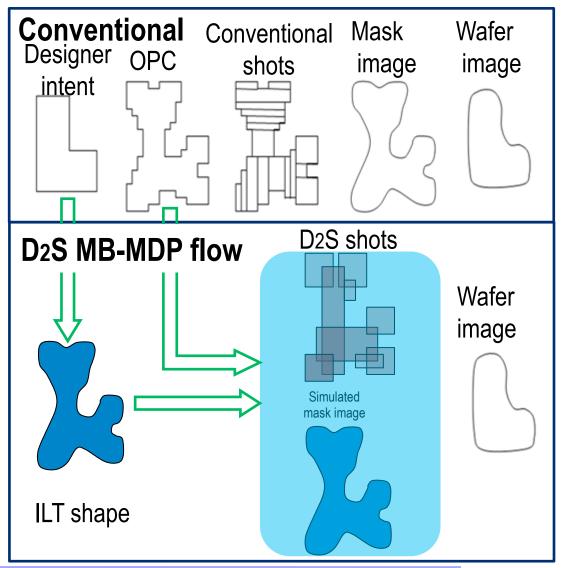


#### **ILT : Biggest Challenge is Mask Writing**



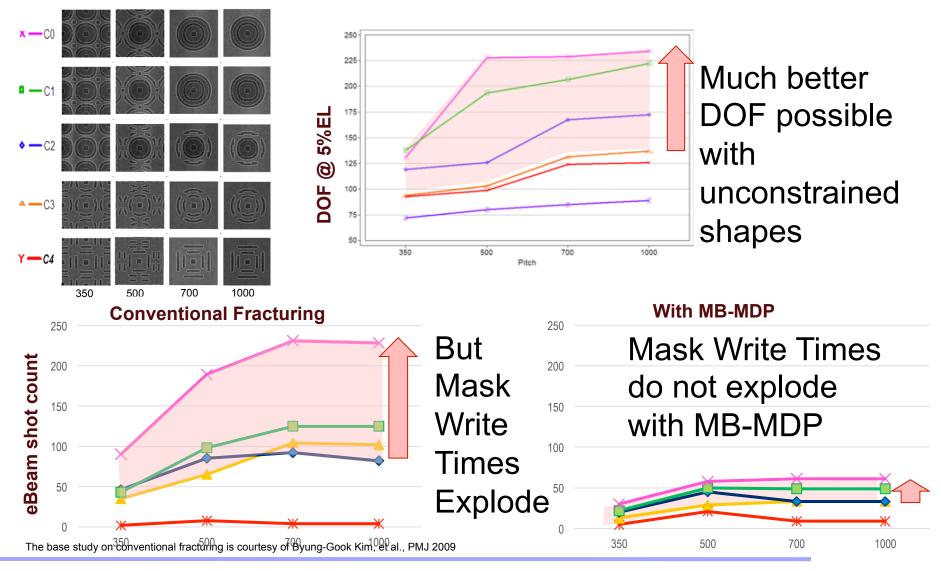
#### Writing Complex Masks Is Possible: MB-MDP (Near Term) and MultiBeam (Long Term)

- MB-MDP benefits over conventional MDP:
  - Model-based, better
    CDU control
  - Utilizes overlapping shots to maximize shot contribution to the final mask shapes
  - Less shot count and better mask fidelity



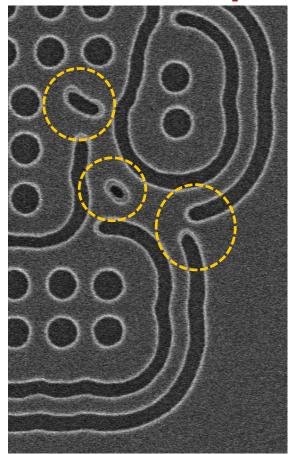


#### **MB-MDP Enables ILT Today**

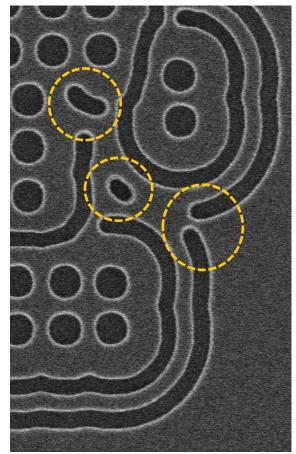


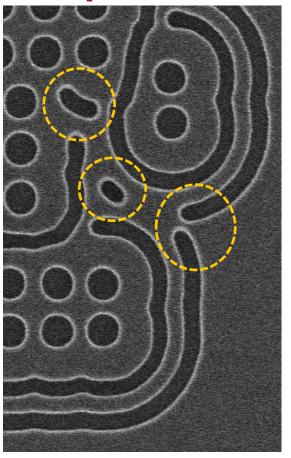
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## Then the Question is: How to Inspect & Review Such Complex Masks



**Conventional MDP** 



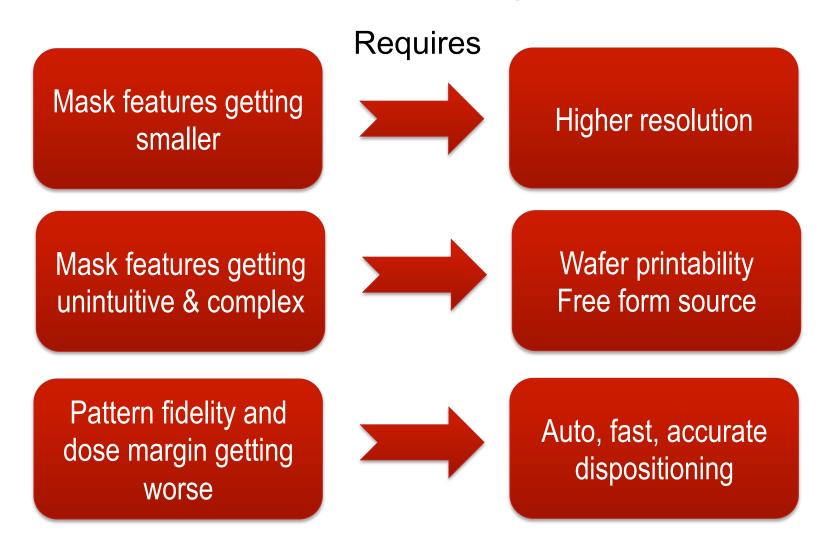


#### Shot Number driven CDU driven MB-MDP MB-MDP B.G. Kim, et al., "Improving CD Uniformity using MB-MDP for 14nm and beyond", BACUS, 2012

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#### **Mask Qualification Challenges**





## Mask Inspection Tools are Adding Aerial Image Inspection Mode





KT Teron<sup>™</sup> 630

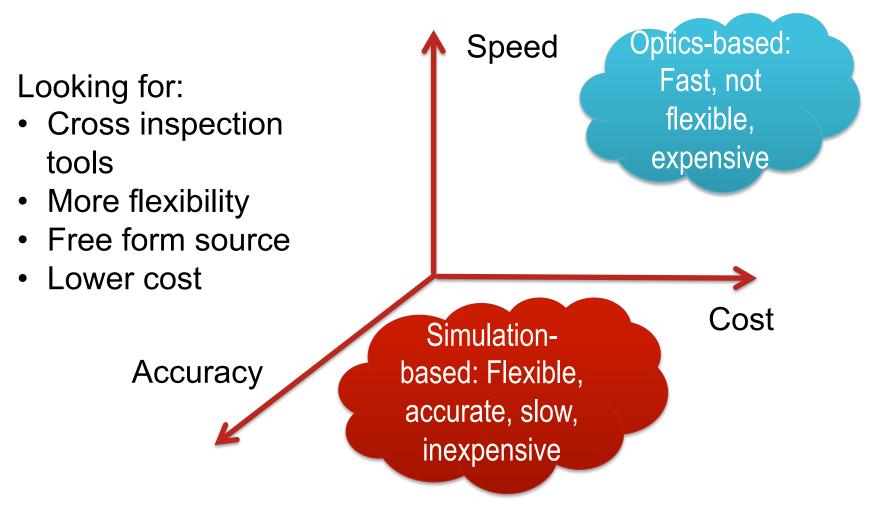
Selectable imaging modes to provide the necessary signal-to-noise ratio (SNR) to ensure defect-free 1Xnm generation reticles, whether optical reticles with **COMPLEX OPC** or EUV reticles (Source: KT website)

#### Applied AERA<sup>™</sup>4

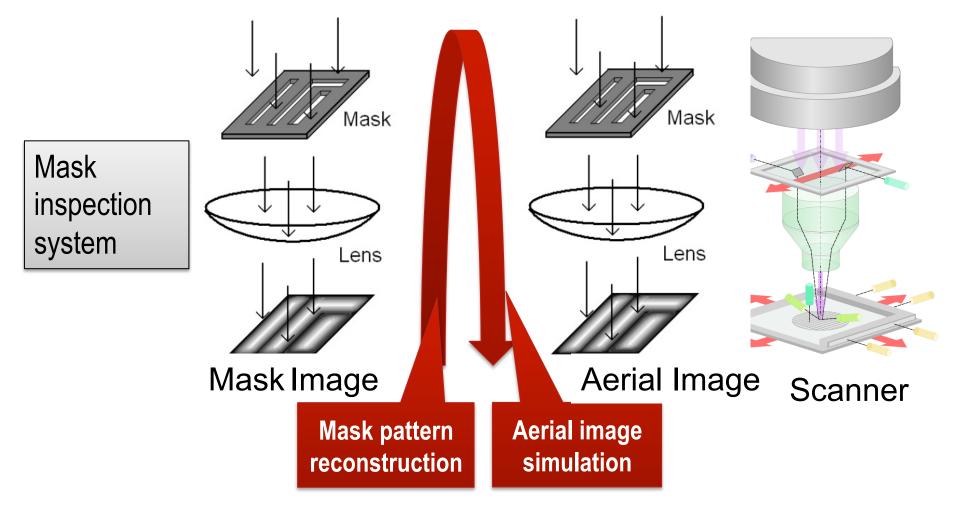
Designed to emulate a scanner, the Aera4 system delivers superior firsttime inspection success rate over other high-resolution inspection systems on advanced masks, including those with aggressive OPC, such as inverse lithography. (Source: KT website)



# Is Optics-Based Aerial Image Inspection Enough? –Not Really

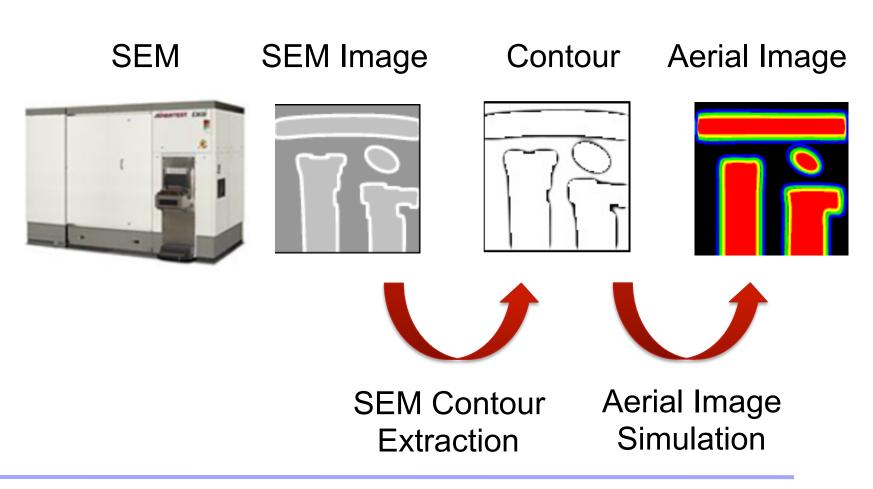


# Simulation Can Provide Aerial Image from <sup>D2S</sup> Mask Inspection Image



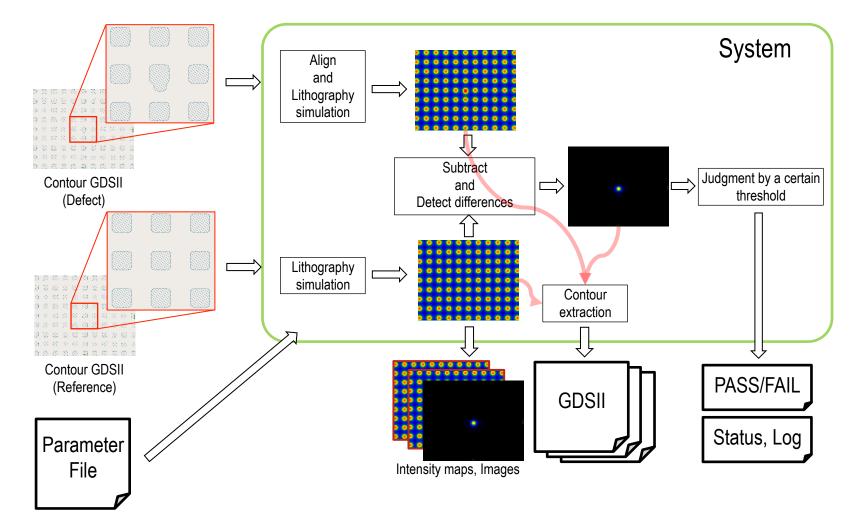


# Simulation Can Also Provide Aerial Image from High Resolution SEM Image





#### Followed by Aerial Image based Defect Review and Dispositioning



# Simulation and Aerial-based Defect Review **Is Already in Production**

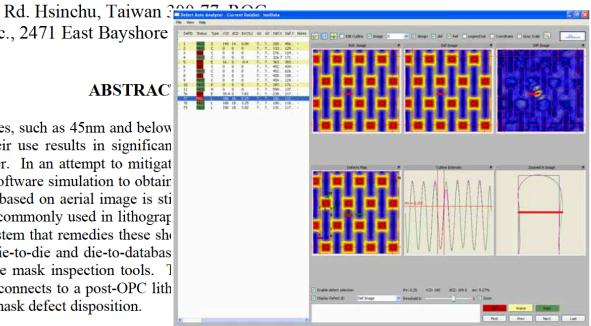
#### Mask Defect Auto Disposition based on Aerial Image in Mask **Production**

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ABSTRAC

At the most advanced technology nodes, such as 45nm and below (SRAFs) are required. However, their use results in significan disposition more challenging than ever. In an attempt to mitigat that rely on hardware emulation and software simulation to obtain however, automatic mask disposition based on aerial image is sti final resist CD or contour, which are commonly used in lithograp automated mask defect disposition system that remedies these she for mask production, works in both die-to-die and die-to-databas AIMS<sup>TM</sup> and aerial-image-based inline mask inspection tools.  $\Box$ plane CD variance. The system also connects to a post-OPC lith CD specs, which are then used in the mask defect disposition.

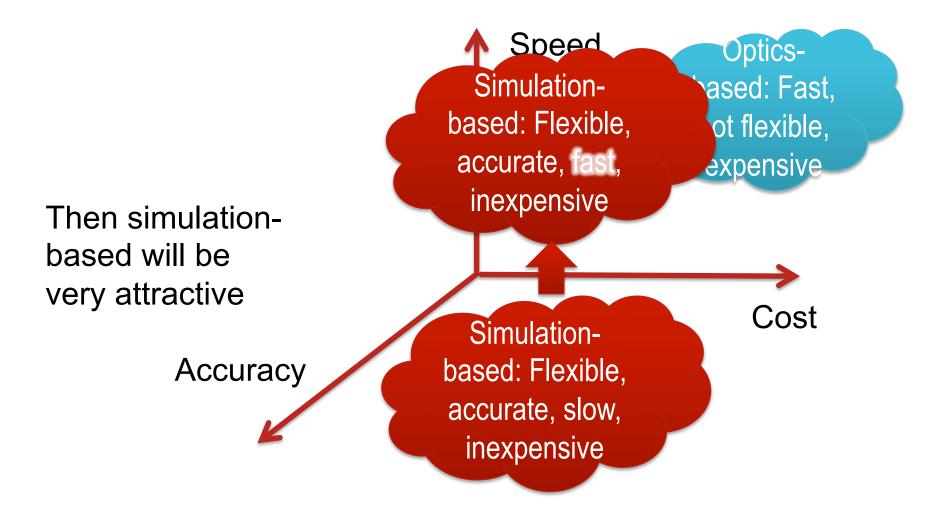


Ref: C. Y. Chen, et al., "Mask defect auto disposition based on aerial image in mask product", Proc. SPIE 7379, Photomask and Next-Generation Lithography Mask Technology XVI, 73791F (May 11, 2009); doi: 10.1117/12.824292; http://dx.doi.org/10.1117/12.824292

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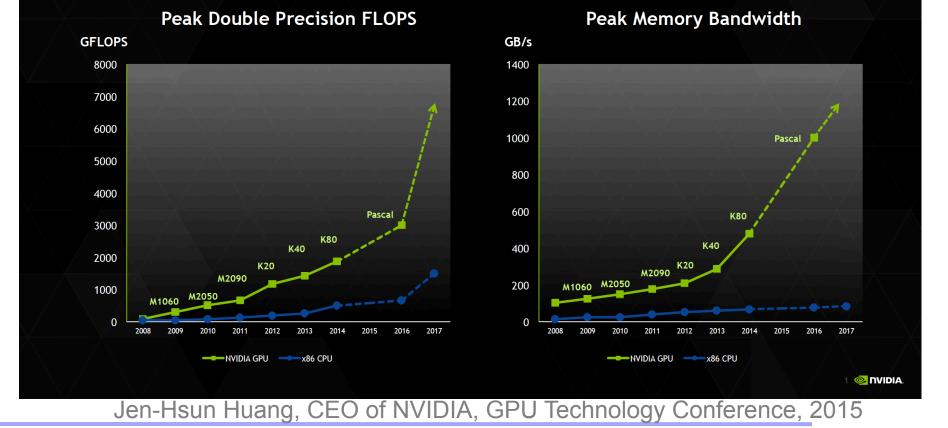


#### **Imagine the Simulation is 10X Faster**



# Scientific Computing Is Moving into GPU: $D_2S$ Ride the Wave or ...

#### PERFORMANCE GAP CONTINUES TO GROW



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# D2S Has Built 400TFLOPS Computational Design Platform Using GPUs



96 Airbus Germany HPC4 - HP POD - Cluster Platform BL460c,21,120400.4456.2Intel Xeon E5-2697v2 12C 2.7GHz, InfinibandFDRFDRHewlett-PackardFOR



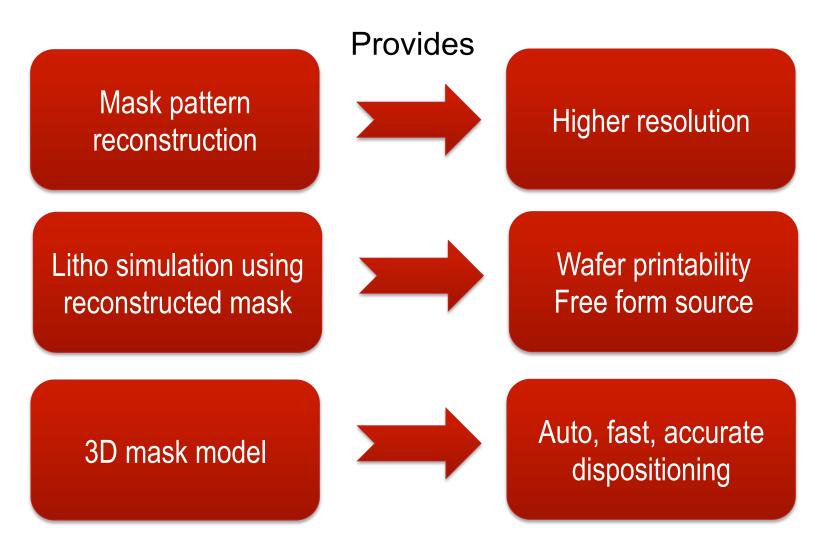
- Could rank in the top 100 Super Computers in the Word (June, 2014)
- In production use
- Simulates the entire mask plane
- All standard parts, with built-in redundancy

DO

The List.



#### **Simulation-Based Solutions with GPU**



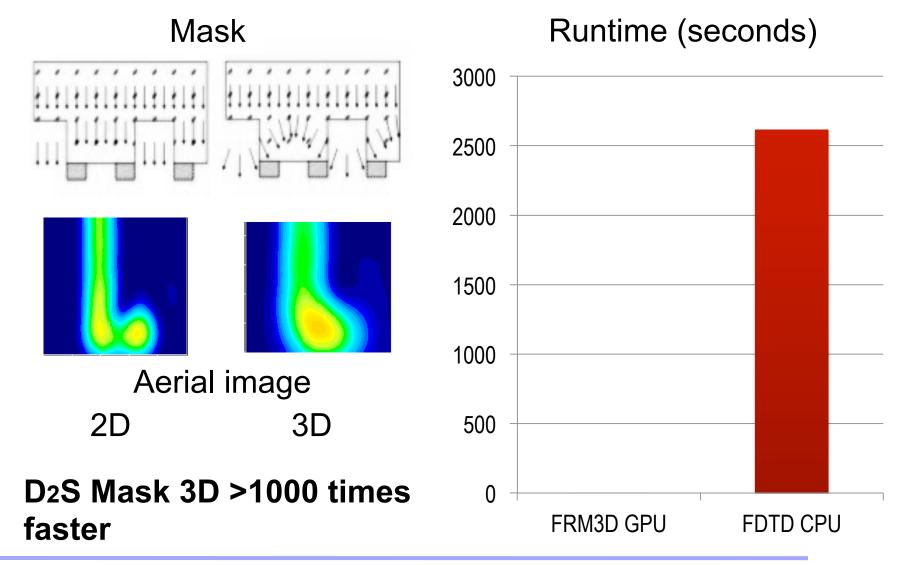


#### With GPU, the Simulation-based Mask Review Applications are Endless

#### With GPU, Mask Hotspots Can Feed to Mask Inspection and Review Using Full Chip Model-Based Mask Verification

- Hotspots due to Fracture:
  - Sliver
  - Extensive overlap shots
  - CD split
- Mask Hotspots
  - Line-end shorting
  - Necking, bridging
  - EPE error
  - Bad dose margin

#### With GPU, Rigorous M3D Simulation Becomes Real Time

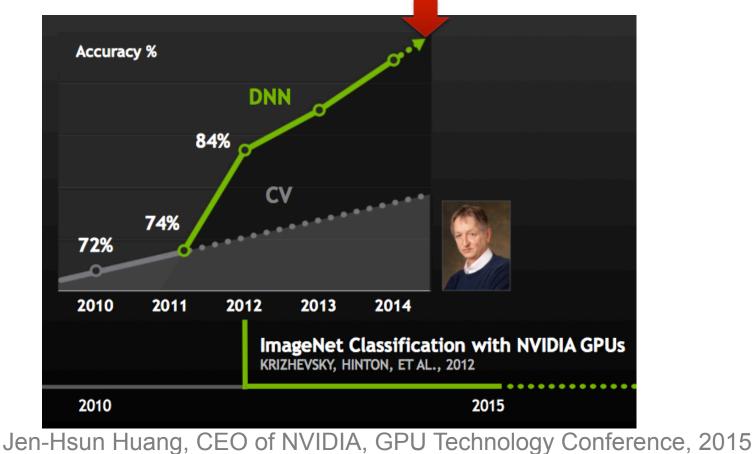


L. Pang, Removing the Last Road Block of Deploying ILT into Production, CSTIC 2015



# With GPU, Deep Learning Can be Applied <sup>L</sup> to Mask Defect Classification

With GPU accelerated deep leaning, the image classification accuracy is better than human now

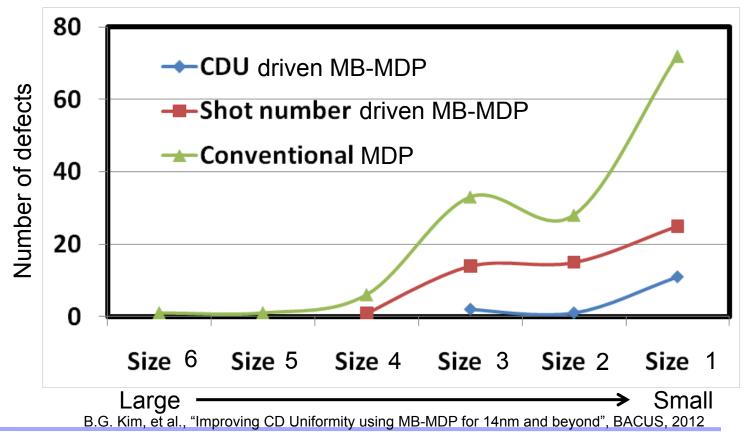


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# Isn't it Better to not Have so Many Mask Defects From the Beginning?

• Both <u>Shot Number</u> and <u>CDU</u>-driven MB-MDP are clearly more effective in reducing size variations.





# Summary: Using GPUs, Complex Mask Shapes Prevail

- Mask makers will be facing complex masks soon
- Simulation-based mask inspection and review helps to solve challenges in mask defect dispositioning
- Scientific computing is moving to GPU
- GPU-accelerated simulation-based mask inspection and review is your best friend
- MB-MDP and Dose Modulation will offload mask inspection and review work

